



江西省晶能半导体有限公司

JiangXi LatticePower Semiconductor Corporation

产品规格书 Specification

产品名称 Product Name: XD2

产品型号 Product P/N: _____

客户 Client name: _____

客户料号 Client P/N: _____

版本号 Version No.: A02

日期 Sending Date: 2023.5.24

客户承认栏 Client Approval	
核准 Approval	确认 Audit

制定 Confirmation: _____ 审核 Approval: _____

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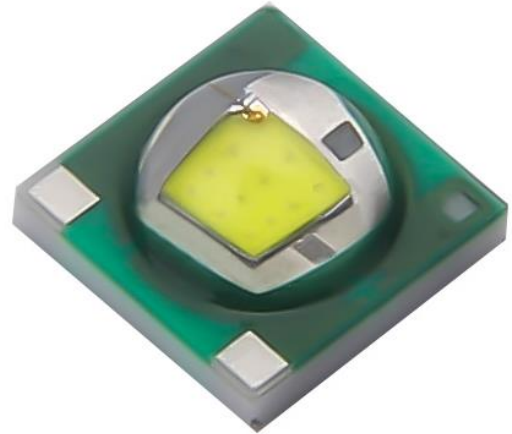


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1、特点 Features

- ◆ 陶瓷封装, 高亮度, 高光效
Ceramic Substrate package , High brightness ,High efficiency
- ◆ 尺寸: 3.45mm*3.45mm
Size: 3.45mm*3.45mm
- ◆ 根据 ANSI 标准分档
According to the ANSI standard colour gamut
- ◆ 350mA@25°C分档
Binned at 350mA@25°C
- ◆ 适于 SMT 贴片
Compatible with SMT
- ◆ 发光角度: 120°
Viewing Angle: 120°
- ◆ 包装: 最大 1000 颗/卷
Package: Max: 1000pcs /reel



2、应用 Applications

建筑照明	Architectural
工业照明	Industrial
商业照明	Commercial
室内外照明	Indoor and outdoor area



3、性能 Performance

a) 绝对最大额定值 Absolute Maximum Ratings

参数 Parameter	符号 Symbol	最大参数值 Maximum Rating	单位 Unit
电流 Forward Current	I_F	500	mA
工作温度 Operating Temperature	T_{opr}	-40~105	°C
存储温度 Storage Temperature	T_{stg}	-40~125	°C
ESD Classification(HBM)		Class 1C(JS-001)	
结温 Junction Temperature	T_j	150	°C

b) 光电参数 Electro-Optical Characteristics ($I_F=350mA, @25^{\circ}C$)





项目 Item	符号 Symbol	最小值 Min.	典型值 Typ.	最大值 Max.	单位 Unit
光通量 Luminous Flux	Φ	Refer to Flux Rank Distribution			Lm
正向电压 Forward Voltage	VF	2.8	2.95	3.2	V
热阻 Thermal Resistance	----	----	9.0	----	°C/W
发光角度 Viewing Angle	2 θ 1/2	----	120	----	°

4、产品代码 Product Order Code

XD - B0 - GA - R2 - AB9
① ② ③ ④ ⑤

- ① 产品型号 Product Type
- ② 显色指数 Ra level
- ③ 色温区块 Colour Area
- ④ 亮度等级 Brightness Level
- ⑤ 电压等级 VF Level

a) 出货标签(例) Shipping label (e.g.)

	LatticePower Corporation Limited
LatticePower	Item: XD-B0-GA-R2-AB9 XD2
	
	Reel ID: AXD000000001
	
Qty: 1000	6XDTEECA
	Date: 20230420

**b) 亮度分档 Luminous Flux Binning (IF =350mA,@25°C)**

Bin	symbol	Min.	Max.	Unit
Q4	IV	100	107	lm
Q5	IV	107	114	
R2	IV	114	122	
R3	IV	122	130	
R4	IV	130	139	
R5	IV	139	148	
S2	IV	148	156	
S3	IV	156	164	

备注 Notes :

◇ 光通量测量误差范围±7%

Luminous flux measurement tolerance: ±7%

c) 电压分档 Forward Voltage Binning (IF =350mA,@25°C)

Bin	symbol	Min.	Max.	Unit
AB8	VF	2.6	2.8	V
AB9	VF	2.8	3.0	
ABA	VF	3.0	3.2	
ABB	VF	3.2	3.4	

备注 Notes :

◇ 电压测量误差范围±0.06V

Tolerance is ±0.06V on voltage measurements

d) 显指分档 Ra Groups (IF =350mA,@25°C)

Bin	symbol	Min.	Max.	R9
B	CRI	60	80	/
B0	CRI	67	80	/
B1	CRI	70	80	/
H1	CRI	80	90	Min.0

备注 Notes :

◇ 显指测量误差范围±2

Tolerance is ±2 on CRI measurements



e) 亮度分布 Flux Rank Distribution (IF =350mA,@25°C)

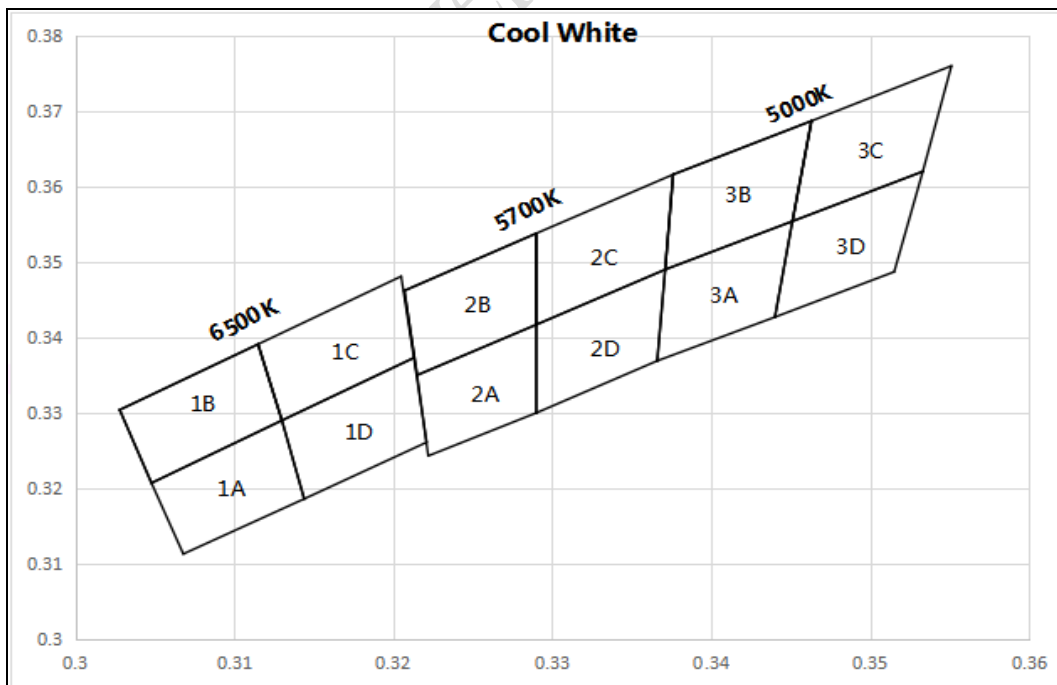
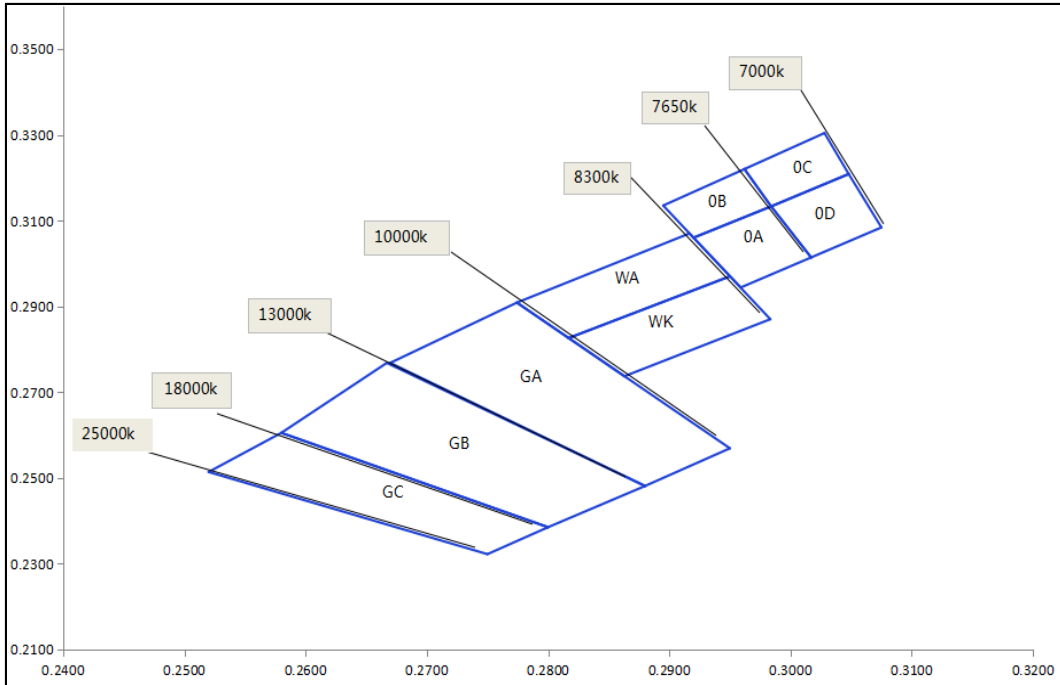
■ 供货等级 Available Rank

CCT	CRI	Flux Rank							
8300-10000K	67	Q4	Q5	R2	R3	R4	R5	S2	S3
7000-8300K	67	Q4	Q5	R2	R3	R4	R5	S2	S3
6500K	67	Q4	Q5	R2	R3	R4	R5	S2	S3
4000K	80	Q4	Q5	R2	R3	R4	R5	S2	S3
3000K	80	Q4	Q5	R2	R3	R4	R5	S2	S3

LatticePower Semiconductor

5、色区分档规则 Color Bin Regulations

a) 色区图 CIE Chromaticity Diagram



d) ANSI Bin 定义 ANSI Color bin definitions

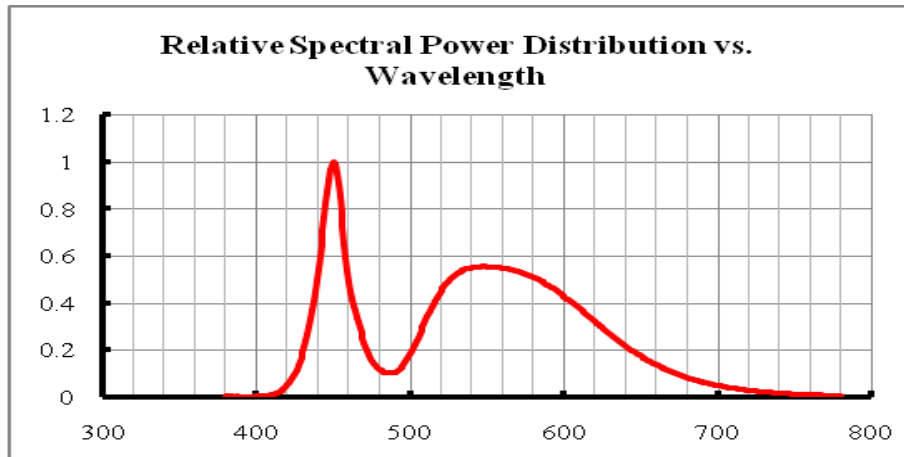
色区	X	Y	色区	X	Y	色区	X	Y	色区	X	Y
GA	0.2950	0.2569	GB	0.2880	0.2481	GC	0.2800	0.2385	GA	0.2950	0.2569
	0.2880	0.2481		0.2800	0.2385		0.2750	0.2322		0.2880	0.2481
	0.2670	0.2768		0.2580	0.2605		0.2520	0.2514		0.2670	0.2768
	0.2774	0.2909		0.2667	0.2768		0.2580	0.2605		0.2774	0.2909
WA	0.2774	0.2909	WK	0.2817	0.2826	OA	0.2984	0.3133	WA	0.2774	0.2909
	0.2917	0.3070		0.2950	0.2970		0.2920	0.3060		0.2917	0.3070
	0.2950	0.2970		0.2983	0.2870		0.2959	0.2944		0.2950	0.2970
	0.2817	0.2826		0.2863	0.2737		0.3017	0.3014		0.2817	0.2826
OB	0.2962	0.3220	OC	0.3028	0.3304	OD	0.3048	0.3209	OB	0.2962	0.3220
	0.2895	0.3135		0.2962	0.3220		0.2984	0.3133		0.2895	0.3135
	0.2920	0.3060		0.2984	0.3133		0.3017	0.3014		0.2920	0.3060
	0.2984	0.3133		0.3048	0.3209		0.3075	0.3084		0.2984	0.3133
1A	0.3048	0.3207	1B	0.3028	0.3304	1C	0.3115	0.3391	1D	0.3130	0.3290
	0.3130	0.3290		0.3115	0.3391		0.3205	0.3481		0.3213	0.3373
	0.3144	0.3186		0.3130	0.3290		0.3213	0.3373		0.3221	0.3261
	0.3068	0.3113		0.3048	0.3207		0.3130	0.3290		0.3144	0.3186
2A	0.3215	0.3350	2B	0.3207	0.3462	2C	0.3290	0.3538	2D	0.3290	0.3417
	0.3290	0.3417		0.3290	0.3538		0.3376	0.3616		0.3371	0.3490
	0.3290	0.3300		0.3290	0.3417		0.3371	0.3490		0.3366	0.3369
	0.3222	0.3243		0.3215	0.3350		0.3290	0.3417		0.3290	0.3300
3A	0.3371	0.3490	3B	0.3376	0.3616	3C	0.3463	0.3687	3D	0.3451	0.3554
	0.3451	0.3554		0.3463	0.3687		0.3551	0.3760		0.3533	0.3620
	0.3440	0.3427		0.3451	0.3554		0.3533	0.3620		0.3515	0.3487
	0.3366	0.3369		0.3371	0.3490		0.3451	0.3554		0.3440	0.3427

备注 Notes :

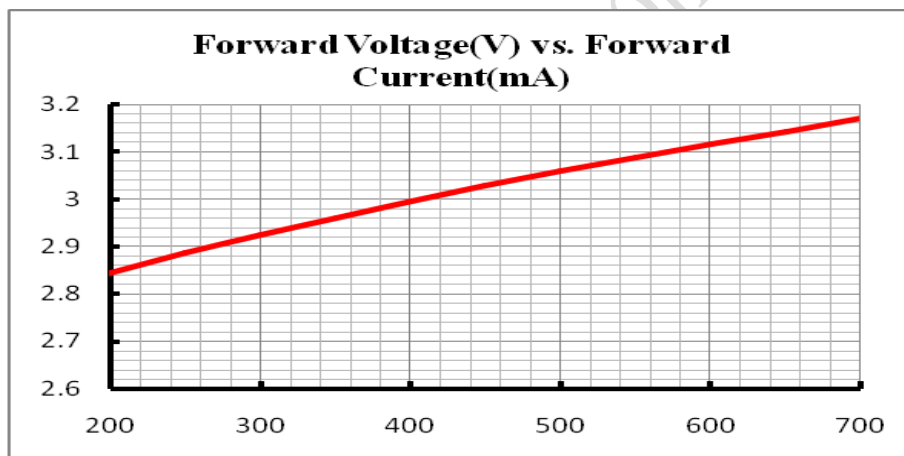
- ◇ 色度坐标 (x, y) 来自 CIE1931 色度图
The chromaticity coordinates(x,y)is derived from the CIE 1931 chromaticity diagram
- ◇ 测试分选设备用于光通量 (lm) 和 CIE1931 色度坐标 (x, y) 测试。
Testing&Sorting Machine is for the luminous flux(lm) and the CIE1931 chromaticity coordinates(x,y) testing.
- ◇ 色度坐标 (x, y) 存在±0.006 公差。
The chromaticity coordinates(x,y) guarantee should be added ±0.006 tolerance.

6、光电特性图 The Photoelectric Characteristics Graph

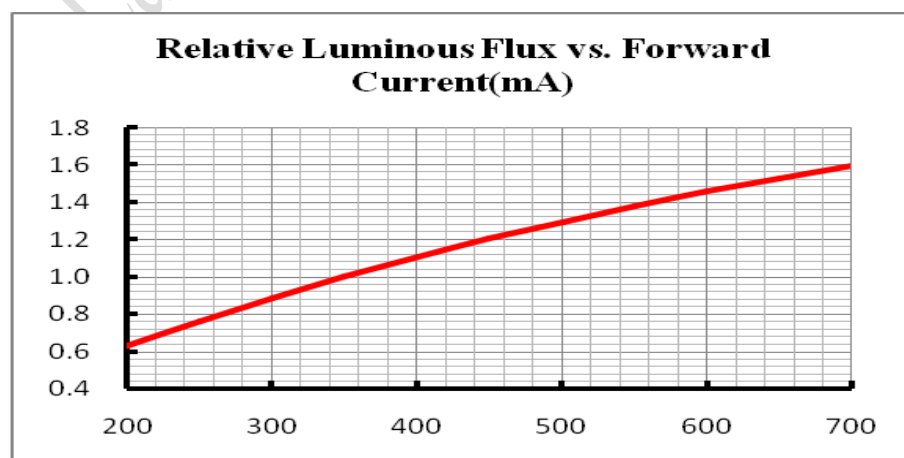
a)、光谱图 Color Spectrum, $T_a=25^\circ\text{C}$

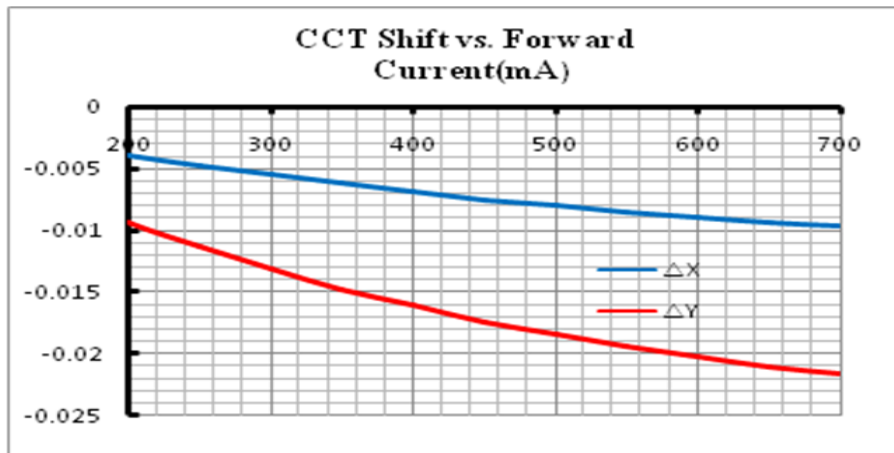
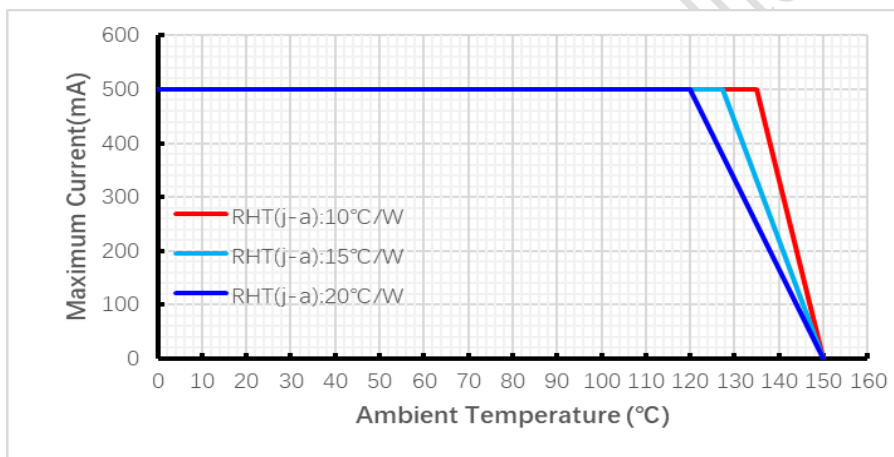


b)、Forward Voltage vs. Forward Current, $T_a=25^\circ\text{C}$



c)、Forward Current vs. Relative Luminous Flux, $T_a=25^\circ\text{C}$



d) 、 Forward Current vs. ΔCCX ΔCCY , $T_a=25^\circ C$

e) 、 Maximum Forward Current vs. Ambient Temperature, $T_j(max.)=150^\circ C$


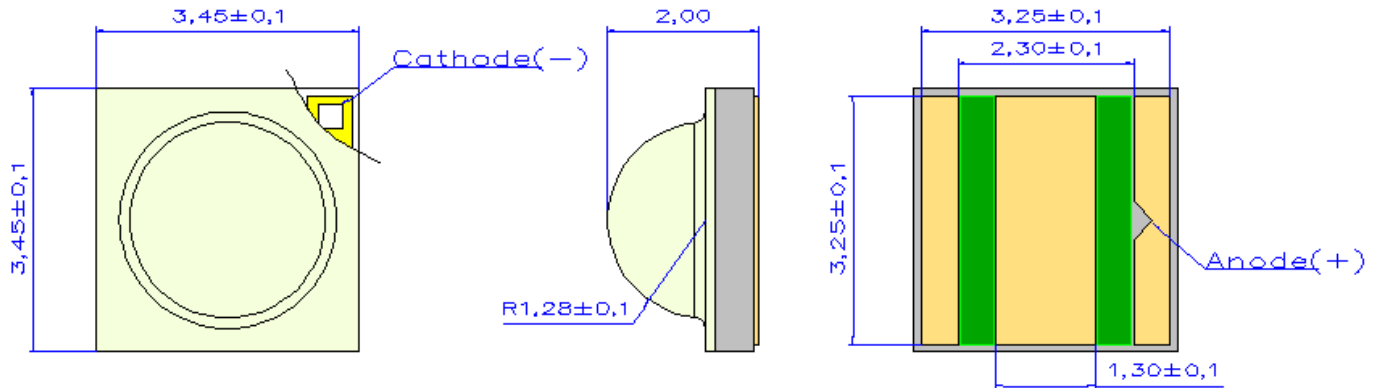
◇ 备注 Notes:

最大正向电流由 LED 结和环境之间的热阻决定。最终产品的设计方式应最大限度地减少从焊点到环境的热阻，以优化灯的寿命和光学特性。

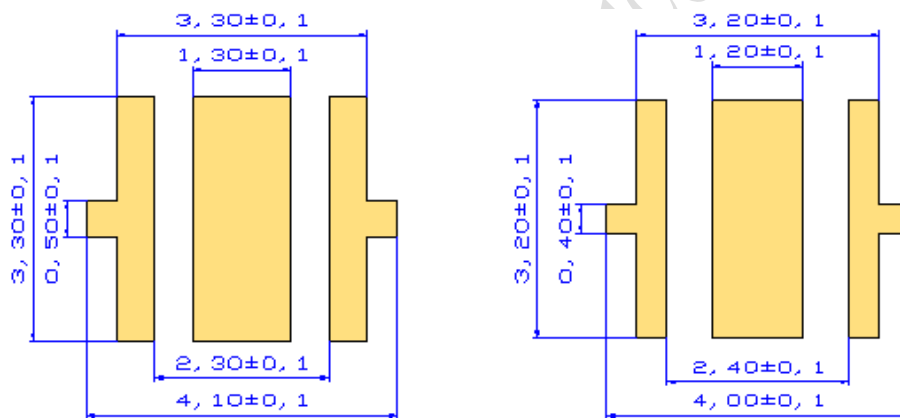
The maximum forward current is determined by the thermal resistance between the LED junction and ambient. It is crucial for the end product to be designed in a manner that minimizes the thermal resistance from the solder point to ambient in order to optimize lamp life and optical characteristics

7、产品及钢网尺寸 Product and PCB Pad Dimensions

Product Dimensions:



PCB Pad Dimensions:



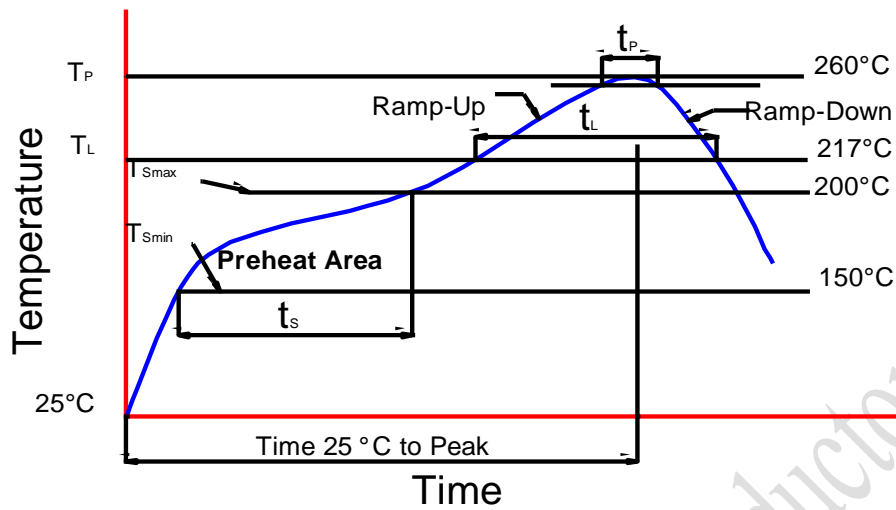
建议 PCB 焊盘

建议钢网

备注 Notes:

- ◇ 所有尺寸均以 mm 为单位
All dimensions are in millimeters
- ◇ 尺寸公差: ± 0.1 mm
Dimension tolerance: ± 0.1 mm

8、回流焊特性 Reflow Soldering Characteristics

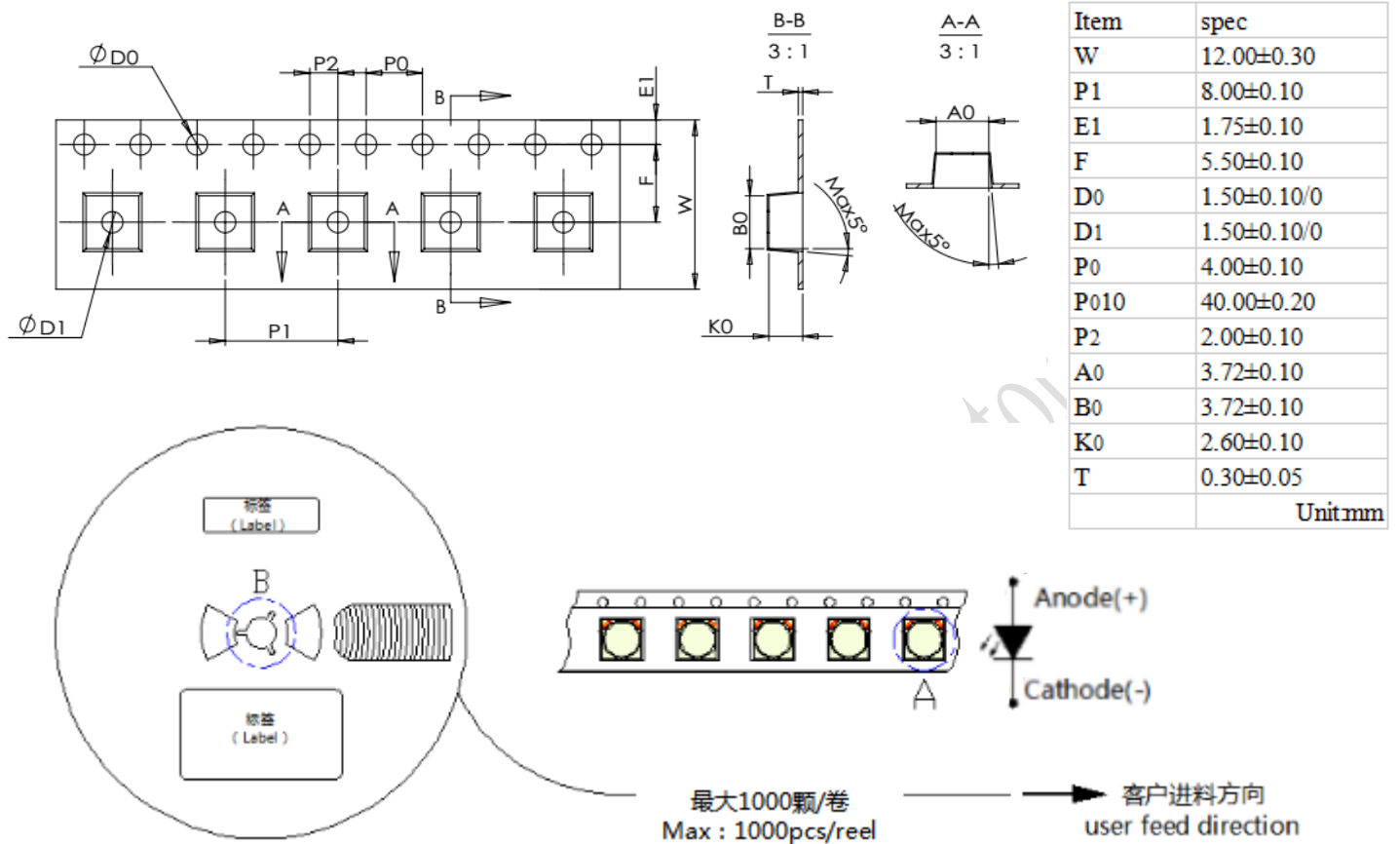


根据 EDEC-J-STD-020D 内容, 参考以下内容。

Compatible with the JEDEC-J-STD-020D, using the parameters listed below.

特制参数 Profile Feature	无铅焊料 Lead-Free Solder
平均上升速率 (T _{Smax} 至 T _p) Average Ramp-Up Rate (T _{Smax} to T _p)	3 °C/sec max.
预热: 温度最小值 (T _{Smin}) Preheat: Temperature Min (T _{Smin})	150
预热: 最高温度 (T _{Smax}) Preheat: Temperature Max (T _{Smax})	200
预热: 时间 (t _{Smin} 到 t _{Smax}) Preheat: Time (t _{Smin} to t _{Smax})	60-180 secs
回流温度 (T _L) Time Maintained Above: Temperature (T _L)	217°C
回流时间 (t _L) Time Maintained Above: Time (t _L)	60-150 secs
峰值/分类温度 (T _p) Peak/Classification Temperature (T _p)	255±5°C
实际峰值温度 (t _p) 在 5°C 以内的时间 Time Within 5°C of Actual Peak Temperature (t _p)	20~40 secs
降低速率 Ramp-Down Rate	5°C/sec max.

9、 卷轴 Reel Dimensions



备注 Notes:

- ◇ 卷轴包装 1000pcs
Reel:1000pcs.
- ◇ 卷轴包装方法符合 IJSC0806 (连续胶带上的电子元件包装)
The tape packing method complies with IJSC0806(Packing of Electronic Components on Continuous Tapes).
- ◇ 当卷轴由于工作中断而重绕时, 载带上压力不应超过 10N, 否则 LED 可能会粘在盖带上
When the tape is rewound due to work interruptions, no more than 10N should be applied to the embossed carrier tape.
The LEDs may stick to the cover tape.

10、可靠性 Reliability

a) 测试和结果 Tests and Results

测试项目 Test Item	参考标准 Reference Standard	测试条件 Test Conditions	测试周期 Test Duration	失效标准 Failure Criteria#	失效数/测试数 Units Failed/Tested
可焊性 (回流焊) Solderability(Reflow Soldering)	JEITA ED=4701 303 303A	$T_{sld}=255\pm 5^{\circ}\text{C}$, 5sec, Lead-free Solder(Sn-3.0Ag-0.5Cu)	3times	#2	0/22
高低温循环 Temperature Cycle	JEITA ED=4701 100 105	-40°C (30min)~ 25°C (5min)~ 85°C (30min)~ 25°C (5min)	100cycles	#1	0/22
高温/低温储存 High/Low Temperature Storage	JEITA ED=4701 200 201/ JEITA ED=4701 200 202	$T_A=120^{\circ}\text{C}/T_A=-40^{\circ}\text{C}$	1000h	#1	0/22
高温老化 High Temperature Operating		$T_A=85^{\circ}\text{C}$, $I_F=500\text{mA}$	1000h	#1	0/22
高温高湿老化 Temperature Humidity Operating		85°C , RH=85%, $I_F=500\text{mA}$	1000h	#1	0/22

b) 失效判定 Failure Criteria

判定 Criteria #	项目 Items	条件 Conditions	失效判定 Failure Criteria
#1	正向电压 Forward Voltage (VF)	I_F	> 初始值×1.1 倍 > Initial value×1.1
	光通量 Luminous Flux (Φ_v)	I_F	< 初始值×0.7 倍 < Initial value×0.7
	反向电流 Reverse Current (IR)	VR=5V	> 1uA > 1uA
#2	回流焊 Solderability	-	焊接面积 < 80% Less than 80% solder coverage

11、注意事项 Cautions

a) 操作注意 Handling Precautions



1) LED 上的压力会影响 led 的可靠性。应采取预防措施，以避免对 led 的强压力。在加热过程中不要对 led 施加压力。

Pressure on the LEDs will influence to the reliability of the LEDs. Precautions should be taken to avoid strong pressure on the LEDs. Do not put stress on the LEDs during heating.

2) LED 焊接完毕后不应重新焊接。如果重新焊接不可避免，那么在修复之前和修复之后都要仔细检查 LED 的特性。

Re-soldering should not be done after the LEDs have been soldered. If re-soldering is unavoidable, LED's characteristics should be carefully checked before and after such repair.

3) 不要将组装好的 pcb 堆在一起。由于 LED 的材料是软的，与 LED 组装的两个 PCB 之间的磨损可能会导致 LED 破坏性的故障。

Do not stack assembled PCBs together. Since materials of LEDs is soft, abrasion between two PCB assembled with LED might cause catastrophic failure of the LEDs.

4) 与标准的封装材料相比，硅胶通常更柔软，表面更容易吸引灰尘，如果不能保证最低程度的灰尘和灰尘颗粒，在部件焊接后，必须在表面使用合适的清洗液。

Compared to standard encapsulants, silicone is generally softer, and the surface is more likely to attract dust, In cases where a minimal level of dirt and dust particles cannot be guaranteed, a suitable cleaning solution must be applied to the surface after the soldering of components.

5) 灯具材料中释放的挥发性有机化合物(VOCs)可以穿透 LED 的硅树脂封装，暴露在热量和光子能量下会变色。结果可能是灯具的光输出有重大损失。

VOCs (Volatile organic compounds) emitted from materials used in the construction of fixtures can penetrate silicone encapsulants of LED and discolor when exposed to heat and photonic energy. The result can be a significant loss of light output from the fixture.

6) 安装 led 时，不要使用释放有机蒸汽的粘合剂。

Attaching LEDs, do not use adhesives that outgas organic vapor.



7) LED 对 ESD (Electro-Static Discharge, ESD)敏感。以下是晶能半导体公司为减少这些影响而提出的一些建议。

LED is sensitive to Electro-Static Discharge (ESD). Below is a list of suggestions that LatticePower Semiconductor purposes to minimize these effects.

a. ESD(静电放电)

静电放电(ESD)定义为两个物体接触时释放出的静电。虽然大多数 ESD 事件被认为是无害的,但在许多工业环境中,在生产和存储期间,它可能是一个代价比较大的问题。ESD 对 LED 的损害可能导致产品显示出不同寻常的特性,例如:一反向泄漏电流的增加降低了开启电压

ESD (Electro Static Discharge)

Electrostatic discharge (ESD) is the defined as the release of static electricity when two objects come into contact. While most ESD events are considered harmless, it can be an expensive problem in many industrial environments during production and storage. The damage from ESD to an LED may cause the product to demonstrate unusual characteristics such as: - Increase in reverse leakage current lowered turn-on voltage.

-LED 在低电流时发射异常 Abnormal emissions from the LED at low current

以下建议可以帮助减少 ESD 发生的可能性。

The following recommendations are suggested to help minimize the potential for an ESD event.

一个或多个工作区域建议:

One or more work area suggestions:

-电离风扇设置 Ionizing fan setup

-导电材料制成的防静电台面/搁板垫 ESD table/shelf mat made of conductive materials

-防静电安全容器 ESD safe storage containers

一个或多个人员建议选择::

One or more personnel suggestion options:

-防静电腕带 Antistatic wrist-strap

-防静电材料鞋 Antistatic material shoes

-防静电衣服 Antistatic clothes

环境控制:

Environmental controls:

-湿度控制(干燥环境下防静电效果较差) Humidity control (ESD gets worse in a dry environment)

b) 清洗 Cleaning

1) 建议使用异丙醇进行清洗。如果使用其他溶剂，必须保证这些溶剂不溶解包装或树脂。不建议使用超声波清洗。超声波清洗可能会对 LED 造成损坏。

We suggests using isopropyl alcohol for cleaning. In case other solvents are used, it must be assured that these solvents do not dissolve the package or resin. Ultrasonic cleaning is not recommended. Ultrasonic cleaning may cause damage to the LED.

c) 存储 Storage

➤ 不要将 LED 放在潮湿的地方，存放温度在 5°C~30°C 之间，相对湿度在 30% 以下。
Do not place the LED in damp places, Storage temperature between 5 °C and 30 °C, Relative humidity under 30%.

➤ 打开包装后使用注意事项 Use Precaution after Opening the Packaging

a) 开包后推荐条件 Recommend conditions after opening the package

- 密封/温度: 5~30°C 湿度: 小于 60%

- Sealing / Temperature : 5 ~ 30°C Humidity : less than RH60%

b) 如果包装已打开超过 168H(MSL 3) 或湿度指示卡颜色发生变化，应在 65±5°C 条件下干燥 10-24H

If the package has been opened more than 168 hours (MSL 3) or the color of the Humidity indicator card changes, components should be dried for 10-24hr at 65±5°C